

In the Specification

The specification has been amended as follows:

Please delete paragraph [0009] on beginning on page 2 and replace it with the following paragraph:

[0009] In Japanese Patent ~~JP71473856A~~ JP7147386A issued to Kozuka Eiji and published on June 6, 1995, entitled "SEMICONDUCTOR DEVICE AND ITS MANUFACTURING METHOD AND APPARATUS USED FOR IT," patterns for two chips, namely one chip of normal type and one chip of reverse type, are formed on the same mask blank. The same data in an integrated circuit data region formed on each semiconductor chip are shared and the direction is the mirror image for the layout; however, the chip is not flipped over for processing where the mirrored image pattern could be used for alignment purposes, nor are any alignment modifications made to the backside.